



22-141 50 SHEETS  
22-142 100 SHEETS  
22-44 200 SHEETS

FIG. 1

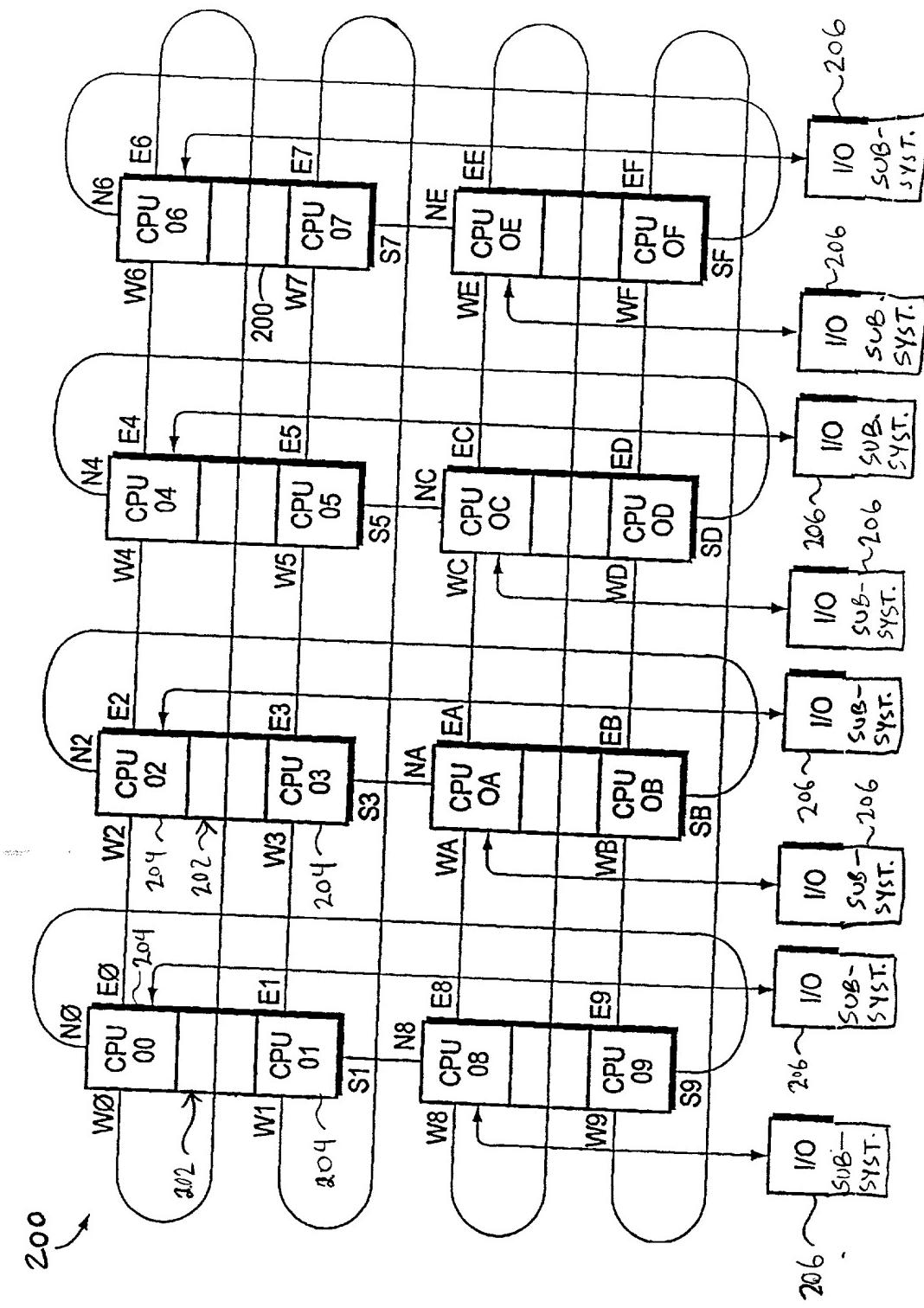
The diagram illustrates the memory mapping for a system with 35 bytes of memory. The bytes are numbered 102 through 118. The mapping is organized into four main sections:

- Byte 32:** ENTRIES R N S 0 TAG REQUESTER BUS NO.
- Bytes 31-30:** 31 { 30 { 29 28 }
- Bytes 24-23:** 24 23 }
- Bytes 11-10:** 11 10 }
- Bytes 16-15:** 16 15 }
- Bytes 8-7:** 08 07 }
- Bytes 4-3:** 08 07 }
- Byte 2:** 00

Each section is associated with a REQUESTER, BUS NO., and FUNCTION NO. The REQUESTER column contains R, N, S, and 0. The BUS NO. column contains TAG, REQUESTER, and REQUESTER. The FUNCTION NO. column contains RES.

BYTE	ENTRIES	R	N	S	0	TAG	REQUESTER	BUS NO.	FUNCTION NO.	REQUESTER	REQUESTER	RES.
35	32	31	30	29	28		24 23		16 15		11 10	
34	33	32	31	30	29	28		24 23		16 15		11 10
33	32	31	30	29	28	27		24 23		16 15		11 10
32	31	30	29	28	27	26		24 23		16 15		11 10
31	30	29	28	27	26	25		24 23		16 15		11 10
30	29	28	27	26	25	24		24 23		16 15		11 10
29	28	27	26	25	24	23		24 23		16 15		11 10
28	27	26	25	24	23	22		24 23		16 15		11 10
27	26	25	24	23	22	21		24 23		16 15		11 10
26	25	24	23	22	21	20		24 23		16 15		11 10
25	24	23	22	21	20	19		24 23		16 15		11 10
24	23	22	21	20	19	18		24 23		16 15		11 10
23	22	21	20	19	18	17		24 23		16 15		11 10
22	21	20	19	18	17	16		24 23		16 15		11 10
21	20	19	18	17	16	15		24 23		16 15		11 10
20	19	18	17	16	15	14		24 23		16 15		11 10
19	18	17	16	15	14	13		24 23		16 15		11 10
18	17	16	15	14	13	12		24 23		16 15		11 10
17	16	15	14	13	12	11		24 23		16 15		11 10
16	15	14	13	12	11	10		24 23		16 15		11 10
15	14	13	12	11	10	9		24 23		16 15		11 10
14	13	12	11	10	9	8		24 23		16 15		11 10
13	12	11	10	9	8	7		24 23		16 15		11 10
12	11	10	9	8	7	6		24 23		16 15		11 10
11	10	9	8	7	6	5		24 23		16 15		11 10
10	9	8	7	6	5	4		24 23		16 15		11 10
9	8	7	6	5	4	3		24 23		16 15		11 10
8	7	6	5	4	3	2		24 23		16 15		11 10
7	6	5	4	3	2	1		24 23		16 15		11 10
6	5	4	3	2	1			24 23		16 15		11 10
5	4	3	2	1				24 23		16 15		11 10
4	3	2	1					24 23		16 15		11 10
3	2	1						24 23		16 15		11 10
2	1							24 23		16 15		11 10
1								24 23		16 15		11 10
0								24 23		16 15		11 10

FIG. 2



202

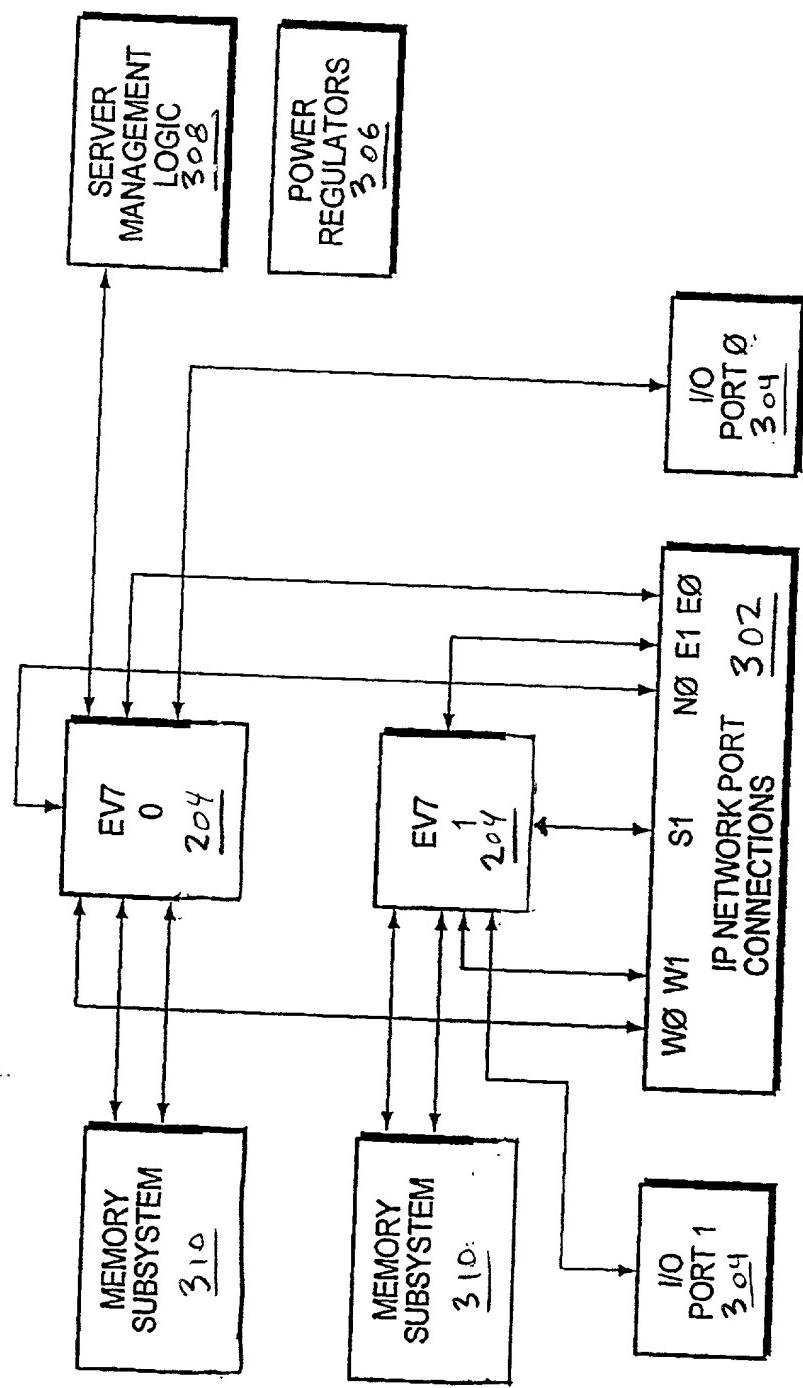
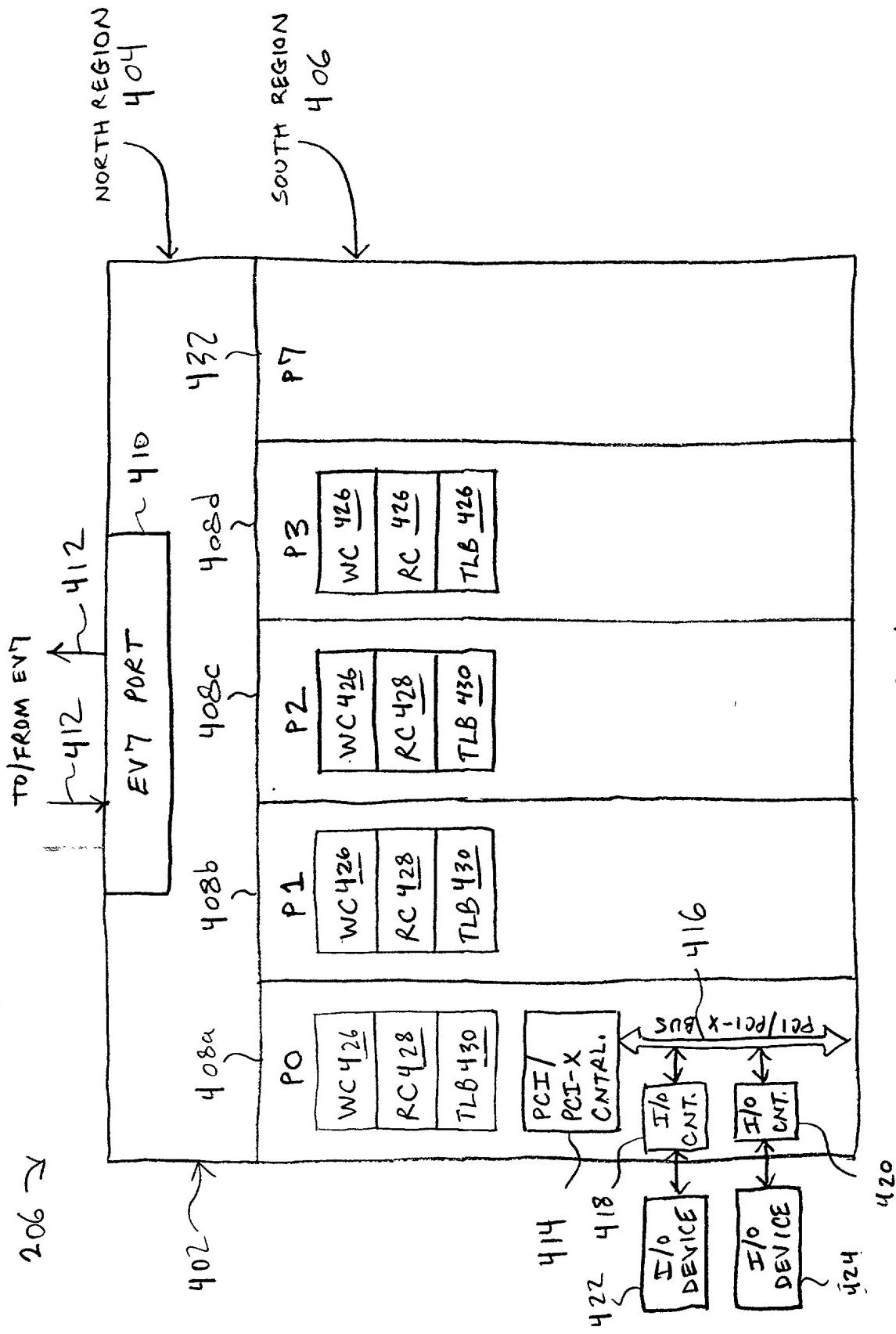


FIG. 3



TO PROCESSORS

FROM PROCESSORS

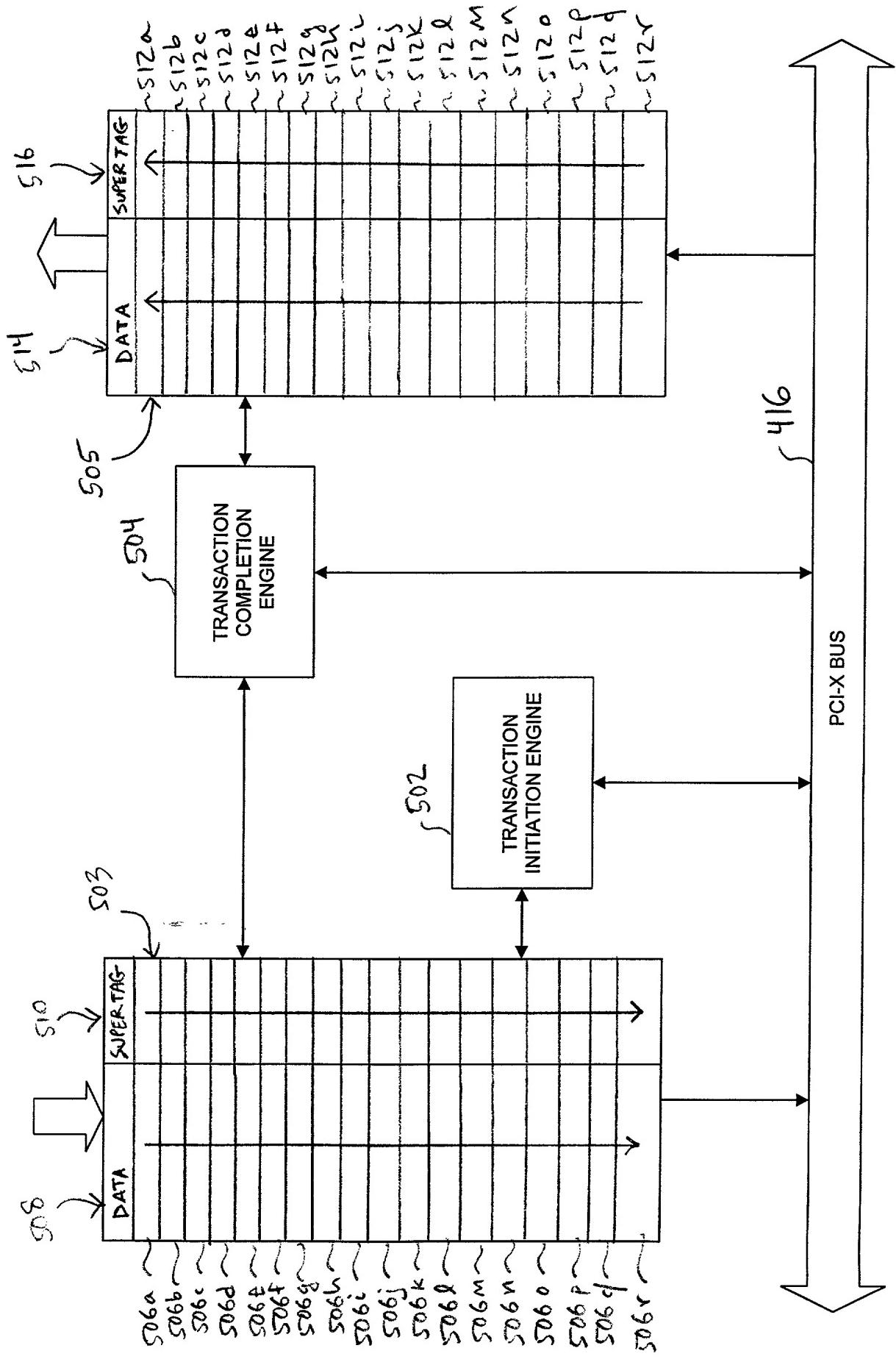
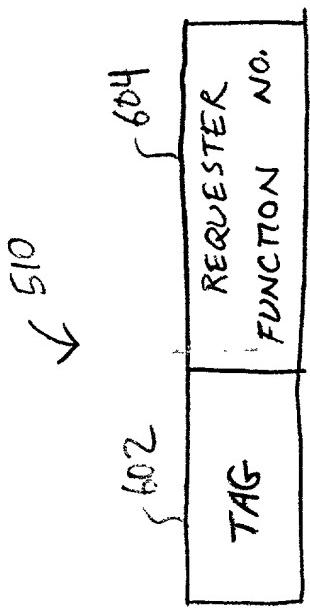


FIG. 5



22-141 50 SHEETS  
22-142 100 SHEETS  
22-144 200 SHEETS

FIG. 6



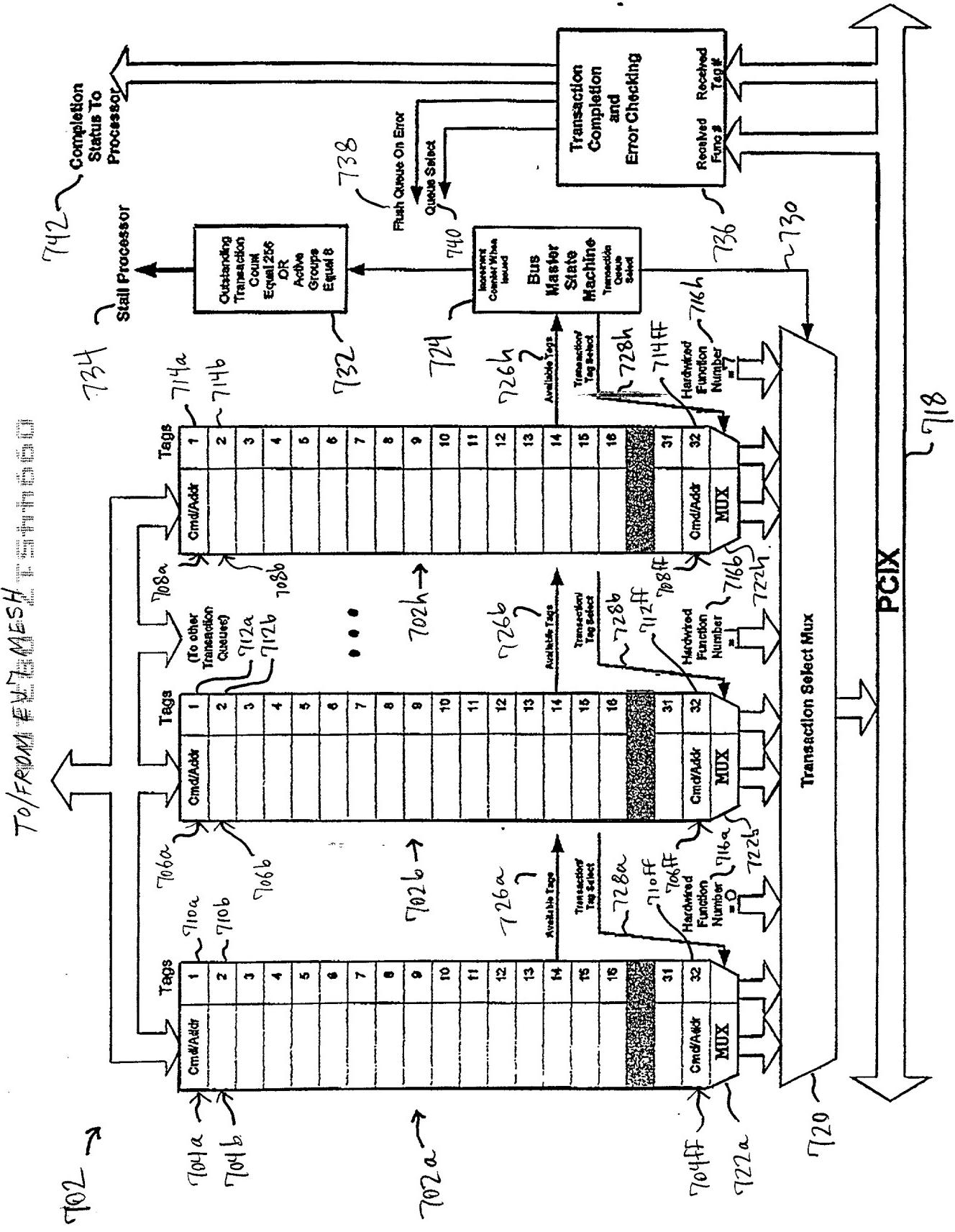


Fig. 7